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# **Cyclone V Soc Fpga D evelopmen t Board Reference Manual Reference Manual**

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*Getting Started  
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SoC Development  
Kit* Cyclone V SoC  
Development Kit  
SoC HPS System

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C/C++ application  
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V SoC Dev Kit using  
ARM DS-5 AE

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Simple FPGA**

**Design** OpenCL on  
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(Linux Host) - Part  
3 - Kernel and Host  
code compilation  
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DE10-Standard  
FPGA-SoC  
Developing Board.  
Preloader and U-  
boot Generation for  
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demonstration ~~The  
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Intel Altera ARM

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Powered FPGA

96Boards

Chameleon96

development board

by Novtech

**EEVblog #635 -  
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**Microcontrollers**

*A Look Inside: SoC*

*FPGAs Introduction*

*(Part 1 of 5) Bit by*

*bit - How to fit 8*

*RISC V cores in a*

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an FPGA, part 1  
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*What Is An FPGA?*

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Cyclone V SoC

FPGA Tutorials with  
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Board - Intro and  
Overview [0]

Getting Started

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~~with Linux on the  
Altera Cyclone V  
SoC Board~~

**Sparklet GUI**

**Library on**

**Intel/Altera**

**Cyclone V SoC**

**FPGA** *Critical Link -*

*Industrial Imaging*

*with Cyclone V SoC*

*Getting Started*

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*Development Kit  
using Linux Video  
Playback Demo on  
Cyclone V SoC*

*Arria 10 SoC  
external U-Boot  
configuration for  
the Golden System  
Reference Design*

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Development~~

The Cyclone ® V  
SoC Development  
Kit offers a quick

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and simple approach to develop custom ARM \* processor-based SoC designs accompanied by Intel's low-power, low-cost Cyclone V FPGA fabric. This kit supports a wide range of functions, such as: Processor and FPGA prototyping and

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power Fpga  
measurement.  
Development  
Industrial  
Board  
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FPGA ...~~

Cyclone® V SoC  
FPGAs offers a  
powerful dual-core  
ARM\* Cortex\*-A9

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MPCore\* processor surrounded by a rich set of peripherals and a hardened memory controller. The FPGA fabric, with up to 110K LEs (logic elements), is connected to the hard processor system (HPS) through a high-speed >100 Gbps

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FPGA Development  
Kit Board 1. Project  
Drawing Numbers:  
Raw PCB Gerber  
Files PCB Design  
Files Assembly  
Drawing Fab  
Drawing Schematic  
Drawing PCB Film  
Bill of Materials  
Schematic Design  
Files Functional  
Specification PCB

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Layout Guidelines  
Assembly Rework  
PCI Express Edge  
Board Connector Cyclone  
V GX SoC Bank 5,6  
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Bank 3,4 ...

~~Cyclone V SoC  
FPGA Development  
Kit Board~~

The Cyclone V SoC  
development board  
provides a

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hardware platform for developing and prototyping low-power, high-performance, and logic-intensive designs using Altera's Cyclone V SoC. The board provides a wide range of peripherals and memory interfaces to facilitate the

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## ~~Cyclone V SoC FPGA Development Board Reference Manual~~

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Development  
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Intel FPGA Cloud  
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Intel Cyclone<sup>®</sup> V  
28nm FPGAs  
provide the  
industry's lowest  
system cost and  
power, along with  
performance levels  
that make the  
device family ideal  
for differentiating

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your high-volume applications. You'll get up to 40 percent lower total power compared with the previous generation, efficient logic integration capabilities, integrated transceiver variants, and SoC FPGA variants with

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an ARM ...

Development  
~~Cyclone® V FPGAs~~  
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Reference  
Manual  
Overview SoCKit -  
the Development  
Kit for New SoC  
Device The SoCKit  
Development Kit  
presents a robust  
hardware design  
platform built  
around the Altera  
Cyclone V System-

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on-Chip (SoC)  
FPGA, which  
combines the latest  
Cortex-A9  
embedded cores  
with industry-  
leading  
programmable  
logic for ultimate  
design flexibility.

~~Terasic SoC  
Platform Cyclone  
SoCKit the ...~~



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FPGA Device •  
Cyclone V SoC  
5CSXFC6D6F31  
Device • Dual-core  
ARM Cortex-A9  
(HPS) • 110K  
Programmable  
Logic Elements •  
5,140 Kbits  
embedded memory  
• 6 Fractional PLLs  
• 2 Hard Memory  
Controllers •  
3.125G

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Configuration and  
Debug • Quad  
Serial  
Configuration  
device – EPCQ256  
on FPGA

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Development Tools  
| Programmable  
Logic ...~~

<http://bit.ly/1IQxpcq>  
Setting up Altera

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OpenCL Run-Time  
Environment on  
Altera Cyclone V  
SoC Development  
Kit Follow Intel  
FPGA to see how  
we're programmed  
for...

~~Getting Started  
with the Cyclone V  
SoC Development  
Kit ...~~

Cyclone V SoC

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Development Kit:  
Cyclone V: 14.0.0 :  
Intel: AN 709: HPS  
SoC Boot Guide -  
Cyclone V SoC  
Development Kit :  
Design Example \  
Outside Design  
Store: Cyclone V  
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Kit: Cyclone V:  
15.0.0 : Intel: AN  
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Hardware

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The Cyclone V SoC  
and its associated  
development kits

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have a comprehensive operating system ecosystem support as listed below: -

Linux is the most common general-purpose operating system used on ARM-based SoCs. The Cyclone V SoC is no different, with comprehensive support offered by

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both Altera and a  
large user  
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Details about

Terasic Cyclone V

DE1-SOC FPGA

Development

Board Kit See

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DE1-SOC FPGA

Development

Board Kit:

Condition: Used.

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Cyclone V SoC is a new SoC tightly coupling with Dual-core ARM Cortex-A9 and FPGA fabric, enabling easy development of complex systems with advanced application processing and flexible hardware

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Development

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The Intel SoC FPGA

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Version 20.1

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and security

updates. Users

should keep their

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and follow the technical recommendations to help improve security. Additional security updates are planned and will be provided as they become available.

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Baseline Pinout :  
Design Example:  
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