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A low noise amplifier is designed for future applications in the 60GHz band, using an existing SiGe technology,

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BiCMOS8HP from IBM. Di erent topologies are analyzed and compared. wTo di erent schematics of single ended three stage designs are compared. A di erential four stage CE topology is designed and sim-ulated with parasitic extraction.

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communications in the 60GHz frequency range. The scope of this paper covers the system design of the OOK direct conversion receiver, the design of a novel 60GHz low-noise amplifier, the co-design of the mixer with the IF amplifier and the design of a IF variable gain amplifier.

~~Design of a low power 60GHz OOK receiver in 65nm CMOS~~

...

The most common wireless applications in 1994 were still broadcast TV and cellular systems. Both of these operated in the much lower range of 600MHz to roughly 2GHz bands. Design and operation of 60GHz mmwave systems was very difficult and expensive. The eco-system of components, test gear and more just did not exist.

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This architecture has been used in the production of a low-cost, 60-GHz module that supported a data rate greater than 155 Mbps. Variations of the complete chip-set architecture can be used to...

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Design and Multiphysics Analysis of Low-Loss 60-GHz SPNT RF-MEMS Switches M. W. Rousstia and M. H. A. J. Herben
Eindhoven University of Technology, Department of Electrical Engineering, Electromagnetics Group, P.O. Box 513, 5600 MB, Eindhoven, The Netherlands, +31 (0)40 247 5287
Abstract — A design of a capacitive-shunt RF-MEMS

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~~Design and multiphysics analysis of low-loss 60-GHz SPNT~~

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Design of 60-GHz low-noise amplifiers with low NF and robust ESD protection in 65-nm CMOS January 2013 IEEE Transactions on Microwave Theory and Techniques 61(1):553-561

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Design and modeling of 60-GHz CMOS integrated circuits.

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~~Design and modeling of 60-GHz CMOS integrated circuits.~~
60GHz, the unilateral gain is 8.6dB while the MSG is 6.3dB. For 60GHz circuit design, the reactive component values are very small, requiring inductance values on the order of 50–150pH. Direct implementation of these elements using spiral inductors do not have the required accuracy, so transmission lines are used with the

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Design and implementation of a 60GHz digitally-controlled passive phase shifter. It consists of a differential transmission loaded with a differential MOS varactor at each side. It

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achieves low cost, simple design, low insertion loss, a phase-shift step of 22.5° and a phase shift range of 360° at 60GHz.

~~Design methods for 60GHz beamformers in CMOS~~

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